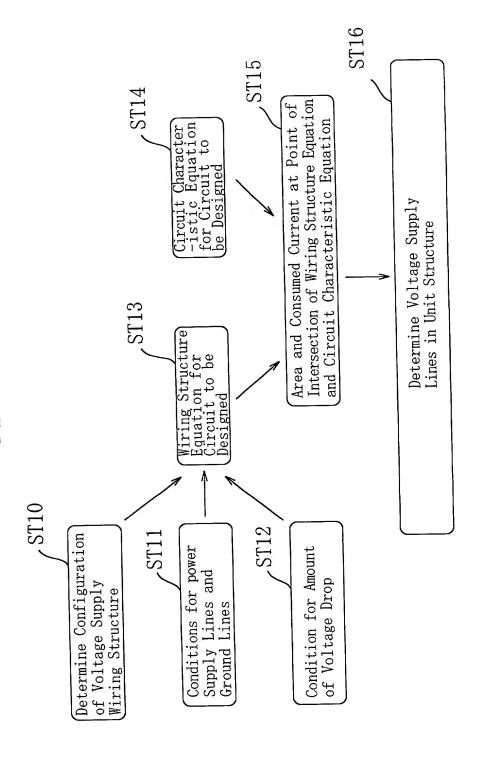
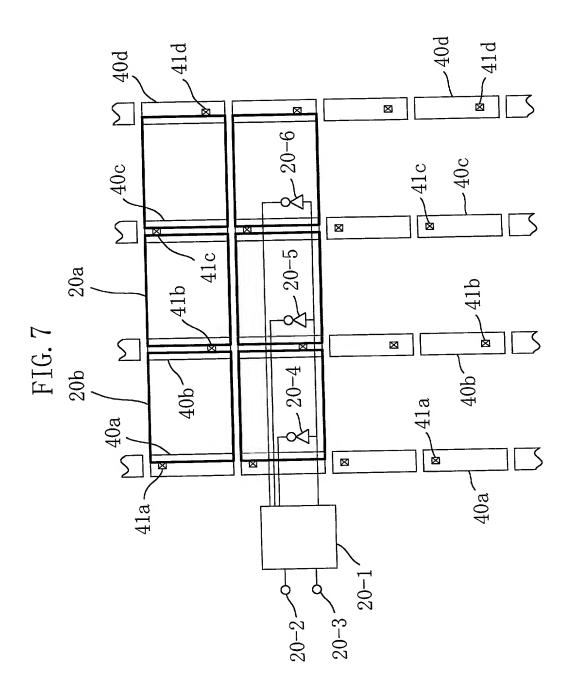
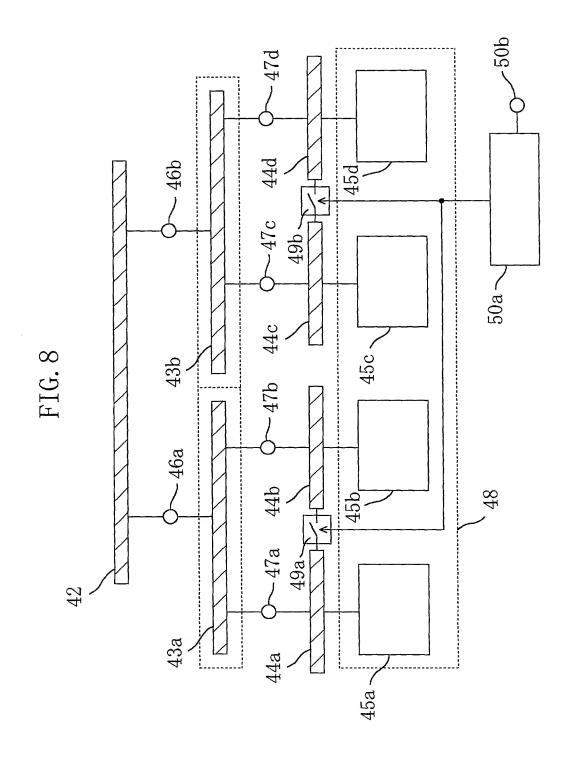
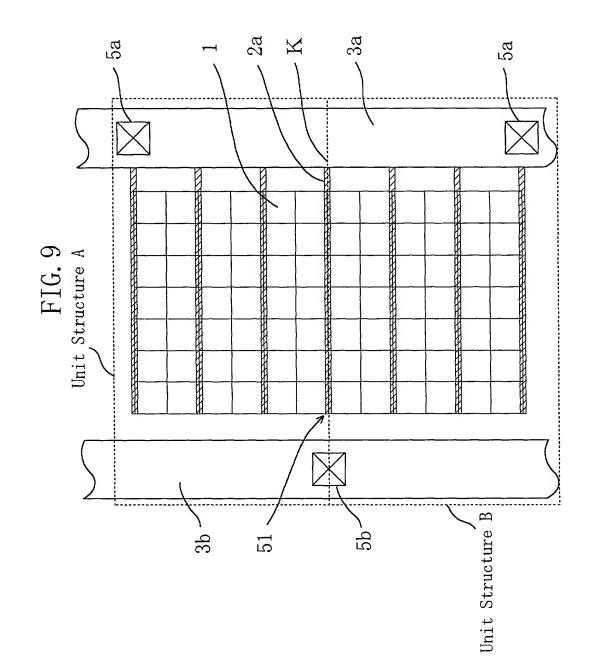


FIG. 6









Cell Placement Region XXX X X X NAX N , Circuit to be Designed NAX-----XXXXXXXX 106a FIG. 10 PRIOR ART 105 XIX xxx less less les des les 106b

107b~

108a~

108b~

-105x

105x

-107b

109a-2

109b-2~

107a <

108a

108b

-107a

109a-1 109b-1